

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	2	(455/20.ccls. 455/333.ccls. 455/326.ccls. 455/323.ccls. 330/300.ccls. 330/311.ccls. 327/113.ccls. 327/359.ccls.) and ((transmitter and receiver) transceiver) and (intermediate adj frequenc\$4) and (frequenc\$5 adj divider) and mixer and downconvert \$3 and upconverter \$3 and (integrated near1 circuit chip)	US-PGPUB; USPAT	OR	ON	2009/04/16 06:48
L5	2	(455/20.ccls. 455/333.ccls. 455/326.ccls. 455/323.ccls. 330/300.ccls. 330/311.ccls. 327/113.ccls. 327/359.ccls.) and (voltage adj control \$4 adj oscillat\$4 (VCO)) and ((transmitter and receiver) transceiver) and (intermediate adj frequenc\$4) and (frequenc\$5 adj divider) and mixer and downconvert \$3 and upconverter \$3 and (integrated near1 circuit chip)	US-PGPUB; USPAT	OR	ON	2009/04/16 06:50

L6	57	((transmitter and receiver) transceiver) and (intermediate adj frequenc\$4) and (frequenc\$5 adj divider) and mixer and downconvert \$3 and upconverter \$3 and (integrated near1 circuit chip)	US-PGPUB; USPAT	OR	ON	2009/04/16 06:51
L8	42	(voltage adj control \$4 adj oscillat\$4 (VCO)) and ((transmitter and receiver) transceiver) and (intermediate adj frequenc\$4) and (frequenc\$5 adj divider) and mixer and downconvert \$3 and upconverter \$3 and (integrated near1 circuit chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 06:52
L10	42	(voltage adj control \$4 adj oscillat\$4 (VCO)) and ((transmitter and receiver) transceiver) and (intermediate adj frequenc\$4) and (frequenc\$5 adj divider) and mixer and downconvert \$3 and upconverter \$3 and (integrated near1 circuit chip) and transmit\$4 and receiv\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 06:54

L11	40	(voltage adj control \$4 adj oscillat\$4 (VCO)) and ((transmitter and receiver) transceiver) and (intermediate adj frequenc\$4) and (frequenc\$5 adj divider) and mixer and downconvert \$3 and upconverter \$3 and demodulat \$4 and (integrated near1 circuit chip) and transmit\$4 and receiv\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 06:55
L12	15	(voltage adj control \$4 adj oscillat\$4 (VCO)) near12 (frequenc\$5 adj divider) and ((transmitter and receiver) transceiver) and (intermediate adj frequenc\$4) and mixer and downconvert\$3 and upconverter\$3 and demodulat\$4 and (integrated near1 circuit chip) and transmit\$4 and receiv\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 06:57
L13	4	(voltage adj control \$4 adj oscillat\$4 (VCO)) near12 (frequenc\$5 adj divider) and ((transmitter and receiver) transceiver) and (intermediate adj frequenc\$4) and mixer and downconvert\$3 and upconverter\$3 and demodulat\$4 and (integrated near1 circuit chip) and transmit\$4 and	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 06:58

		receiv\$4 and ((local adj oscillat \$4) near12 (frequenc\$5 adj divider))				
L14	4	(voltage adj control \$4 adj oscillat\$4 (VCO)) near12 (frequenc\$5 adj divider) and (receiver transceiver) near22 (intermediate adj frequenc\$4) and mixer and downconvert\$3 and upconverter\$3 and demodulat\$4 and (integrated near1 circuit chip) and transmit\$4 and receiv\$4 and ((local adj oscillat \$4) near12 (frequenc\$5 adj divider))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 07:03
L15	1	(receiver transceiver) near22 (intermediate adj frequenc\$4) and downconvert\$3 and upconverter\$3 and demodulat\$4 and (integrated near1 circuit chip) and transmit\$4 and receiv\$4 and ((local adj oscillat \$4) near22 (frequenc\$5 adj divider) near22 mixer near22 (voltage adj control \$4 adj oscillat\$4 (VCO)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 07:28

L16	1	(receiver transceiver) near22 (intermediate adj frequenc\$4) and downconvert\$3 and upconverter\$3 and demodulat\$4 and (integrated near1 circuit chip) and transmit\$4 and receiv\$4 and ((local adj oscillat \$4 LO) near22 (frequenc\$5 adj divider) near22 mixer near22 (voltage adj control \$4 adj oscillat\$4 (VCO)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/16 07:29
S9	0	(local adj oscillat \$5) and (voltage adj control\$3 adj oscillat\$5) and (intermedia adj frequenc\$3) and transmitter and receiver and (frequenc\$5 adj divider) and mixer and downconvert \$3 and upconverter \$3 and (integrated near1 circuit)	US-PGPUB; USPAT	OR	ON	2009/01/07 23:16
S12	0	(local adj oscillat \$5) and (voltage adj control\$3 adj oscillat\$5) and (intermedia adj frequenc\$3) and transmitter and receiver and (frequenc\$5 adj divider) and mixer and downconvert \$3 and upconverter \$3 and (integrated near1 circuit chip)	US-PGPUB; USPAT	OR	ON	2009/01/07 23:17

S13	476	mixer and downconvert\$3 and upconverter\$3 and (integrated near1 circuit chip)	US-PGPUB; USPAT	OR	ON	2009/01/07 23:18
S14	2	(local adj oscillat \$5) and (voltage adj control\$3 adj oscillat\$5) and (intermedia adj frequenc\$3)	US-PGPUB; USPAT	OR	ON	2009/01/07 23:18
S15	7680	(local adj oscillat \$5) and (voltage adj control\$3 adj oscillat\$5)	US-PGPUB; USPAT	OR	ON	2009/01/07 23:18
S16	0	(local adj oscillat \$5) and (voltage adj control\$3 adj oscillat\$5) and (intermedia adj frequenc\$3) and ((transmitter and receiver) transceiver) and (frequenc\$5 adj divider) and mixer and downconvert \$3 and upconverter \$3 and (integrated near1 circuit chip)	US-PGPUB; USPAT	OR	ON	2009/01/07 23:19
S18	0	(local adj oscillat \$5) and (voltage adj control\$3 adj oscillat\$5) and (intermedia adj frequenc\$3) and ((transmitter and receiver) transceiver) and (frequenc\$5 adj divider) and mixer and (integrated near1 circuit chip)	US-PGPUB; USPAT	OR	ON	2009/01/07 23:30

S19	0	(local adj oscillat\$5) and (voltage adj control\$3 adj oscillat\$5) and (intermedia adj frequenc\$3) and ((transmitter and receiver) transceiver) and (frequenc\$5 adj divider) and mixer and (integrated near1 circuit\$3 chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 23:30
S20	0	(local adj oscillat\$5) and (voltage adj control\$3 adj oscillat\$5) and (intermedia adj frequenc\$3) and ((transmitter and receiver) transceiver antenna) and (frequenc\$5 adj divider) and mixer and (integrated near1 circuit\$3 chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 23:31
S21	2	(local adj oscillat\$5) and (voltage adj control\$3 adj oscillat\$5) and (intermedia adj frequenc\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 23:34
S22	56	((transmitter and receiver) transceiver) and (frequenc\$5 adj divider) and mixer and downconvert\$3 and upconverter\$3 and (integrated near1 circuit chip)	US-PGPUB; USPAT	OR	ON	2009/01/07 23:34

S23	0	(local adj oscillat \$5) and (voltage adj control\$3 adj oscillat\$5) and (intermedia adj frequenc\$3) and ((transmitter and receiver) transceiver antenna) and (frequenc\$5 adj divider) and mixer and (integrated near1 circuit\$3 chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 23:35
S24	0	(local adj oscillat \$5) and (voltage adj control\$3 adj oscillat\$5) and (intermedia adj frequenc\$3) and ((transmitter and receiver) transceiver antenna) and (divider) and mixer and (integrated near1 circuit\$3 chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 23:35
S26	0	(local adj oscillat \$5) and (voltage adj control\$3 adj oscillat\$5 cvo) and (intermedia adj frequenc\$3) and ((transmitter and receiver) transceiver antenna) and (divider) and mixer and (integrated near1 circuit\$3 chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 23:44

S27	2	(local adj oscillat\$5) and (voltage adj control\$3 adj oscillat\$5 cvo) and (intermedia adj frequenc\$3) and ((transmitter and receiver) transceiver antenna) and (divid\$4) and mix\$3 and (integrated near1 circuit\$3 chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/07 23:45
S28	0	(local adj oscillat\$5) and (voltage adj control\$3 adj oscillat\$5) and divider and (intermedia adj frequenc\$3)	USPAT	OR	ON	2009/01/21 03:21
S29	0	(local adj oscillat\$5) and (voltage adj control\$3 adj oscillat\$5) and divider and (downconvert\$3 down adj convert\$3) and (up adj convert\$3 upconvert\$3)	USPAT	OR	ON	2009/01/21 03:23
S30	5	(local adj oscillat\$5) and (voltage adj control\$3 adj oscillat\$5) and devid\$3 and (downconvert\$3 down adj convert\$3) and (up adj convert\$3 upconvert\$3)	USPAT	OR	ON	2009/01/21 03:23
S31	5	(local adj oscillat\$5) and (voltage adj control\$3 adj oscillat\$5) and devid\$3 and mix\$3 and (downconvert\$3 down adj convert\$3) and (up adj convert\$3 upconvert\$3)	USPAT	OR	ON	2009/01/21 03:24

S32	6	(local adj oscillat\$5) and (voltage adj control\$3 adj oscillat\$5) and devid\$3 and mix\$3 and (downconvert\$3 down adj convert\$3) and (up adj convert\$3 upconvert\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/14 22:05
S33	0	(Ahmadreza near3 Rofougaran Maryam near3 Rofougaran Brima near3 Ibrahim Jacob near3 Rael Shahla near3 Khorram Shervin near3 Moloudi Stephen near3 Wu Hooman near3 Darabi William near3 Collieran Ed near3 Chien Meng near3 An near3 Pan) and (local adj oscillat\$5) and (voltage adj control\$3 adj oscillat\$5) and devid\$3 and mix\$3 and (downconvert\$3 down adj convert\$3) and (up adj convert\$3 upconvert\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/14 22:18
S34	6	(oscillat\$5) and (voltage adj control\$3 adj oscillat\$5) and devid\$3 and mix\$3 and (downconvert\$3 down adj convert\$3) and (up adj convert\$3 upconvert\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/15 04:35

S35	6	(voltage adj control \$3 adj oscillat\$5) and devid\$3 and mix\$3 and (downconvert\$3 down adj convert \$3) and (up adj convert\$3 upconvert\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/15 04:36
S36	4	(455/20.ccls. 455/333.ccls. 455/326.ccls. 455/323.ccls. 330/300.ccls. 330/311.ccls. 327/113.ccls. 327/359.ccls.) and ((transmitter and receiver) transceiver) and (frequenc\$5 adj divider) and mixer and downconvert \$3 and upconverter \$3 and (integrated near1 circuit chip)	US-PGPUB; USPAT	OR	ON	2009/04/15 05:40

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